

[illegible][illegible]

OPTION INDEX	
APP OR WRG	LOCATION
Z	2C3
Y	2C3
X	2F1,3A1
W	2F1,3A7
V	APP FIG. 2
U	APP FIG. 2

DIRECT CURRENT ORAIN

NOMINAL VOLTAGE
48 VOC

CURRENT

.055 - .075 AMPS

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

SUPPORTING INFORMATION

	CATEGORY	NO.
f		

NOTICE
NOT FOR USE OR DISCLOSURE
OUTSIDE THE BELL SYSTEM
EXCEPT UNDER WRITTEN AGREEMENT

ISSUE
16E

1149

COGNITIVE SYSTEMS

~~J99329A-CONTROL UNIT (60A)-~~
~~CIRCUIT~~

②

SD-99331-01-A1

14 SHEETS

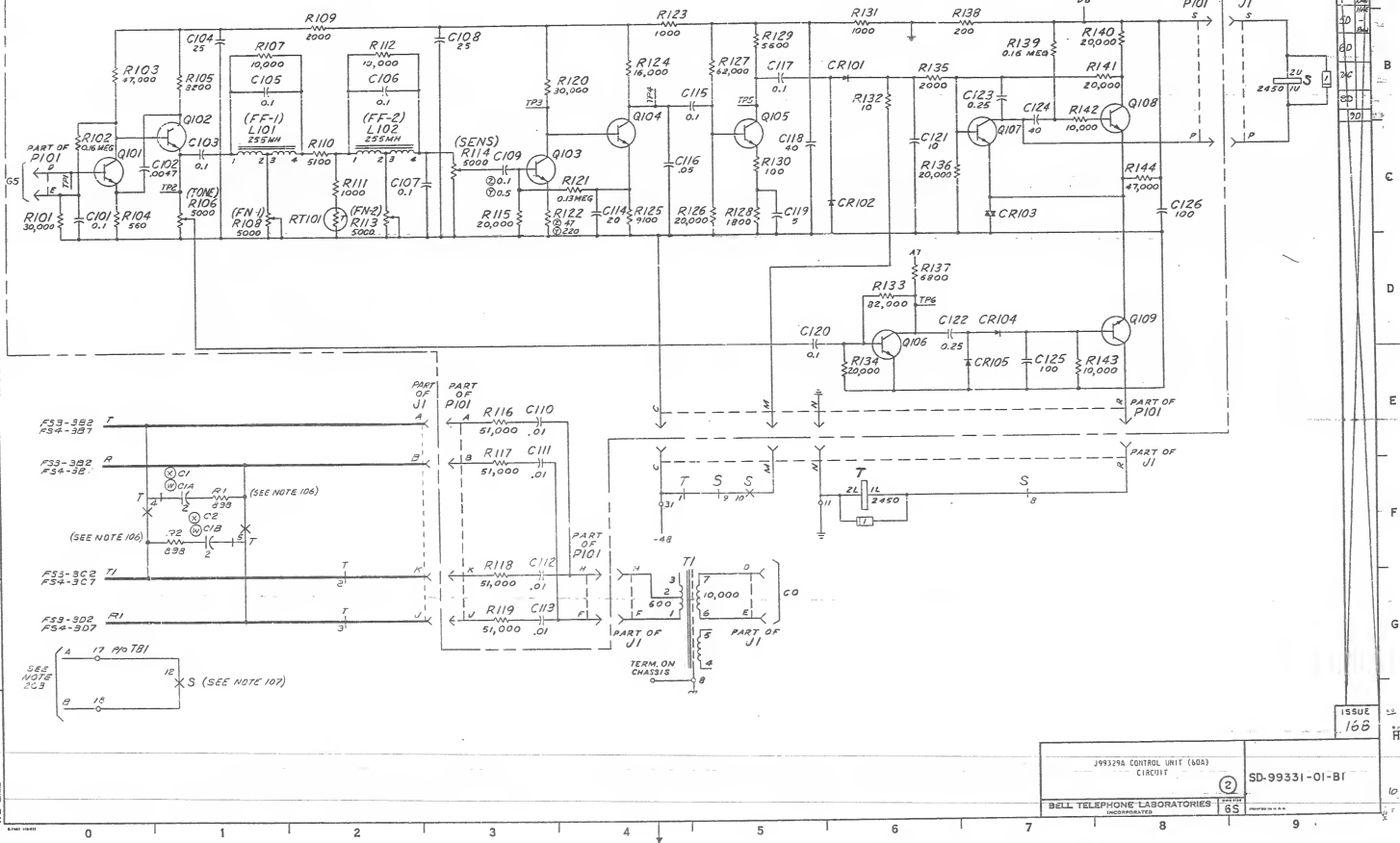
BELL TELEPHONE LABORATORIES

66

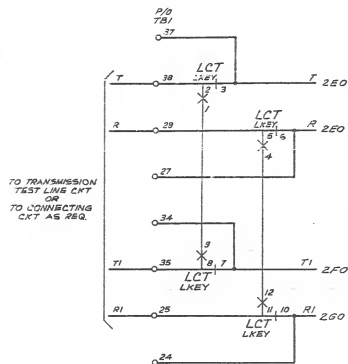
PRINTED BY S.

FS 1 (MFR DISC) CONTROL CIRCUIT

FS 2 (MFR DISC) AMPLIFIER, DETECTOR CIRCUIT

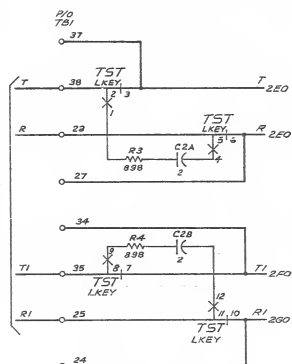


LOOP CUT-THROUGH KEY CIRCUIT



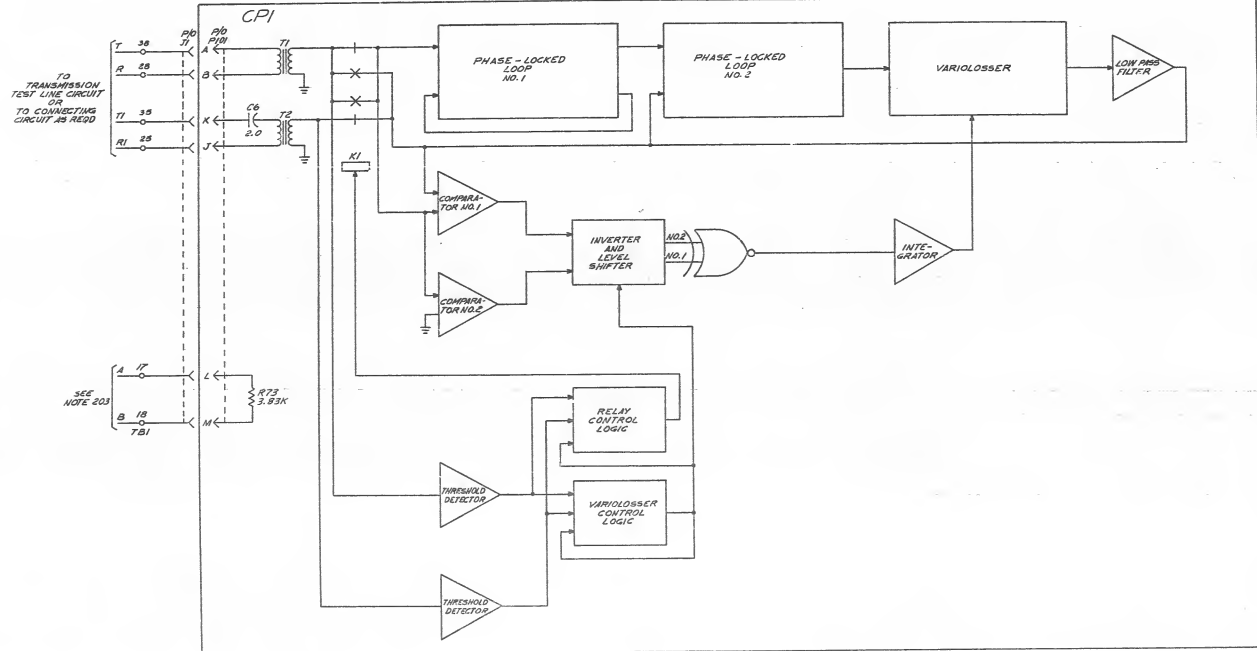
TO TRANSMISSION
TEST LINE CKT
OR
TO CONNECTING
CKT AS REQ.

LOOP TERMINATING KEY CIRCUIT



TO TRANSMISSION
TEST LINE CKT
OR
TO CONNECTING
CKT AS REQ.

FS5 CONTROL CIRCUIT



ISSUE
168

J93256 CONTROL UNIT (600)
CIRCUIT

S0-99331-01-B3

BELL TELEPHONE LABORATORIES
INCORPORATED

65

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APP FIG. 5

CIRCUIT PACK

DESIG	LOC	CODE
CP1	3A1	ED-99653-32

CONNECTOR

DESIG	LOC	CODE
J1	3A1	K5-16345, L2 (SOCKET)

TERMINAL STRIP

DESIG	LOC	CODE
TB1	301	D3A

APP FIG. 6

CIRCUIT PACK

DESIG	LOC	CODE
CP1A	3A1	ED-99653-33

CONNECTOR

DESIG	LOC	CODE
J1	3A1	K5-16345, L2 (SOCKET)

TERMINAL STRIP

DESIG	LOC	CODE
TB1	301	D3A

J9329A CONTROL UNIT (60A)
CIRCUITBELL TELEPHONE LABORATORIES
INCORPORATED

65

SD-99331-01-C2

ISSUE
16B

CIRCUIT NOTES:

DESIG.	FUSE AMP	POTENTIAL	ONE PER
		40Y TALK	FIG. 1
BATTERY SYMBOL		VOLTAGE RANGE	
-48		42.75 TO 52.5	

FEATURE OR OPTION	APP FIG.	TOP OR BOT.	QUANTITY
60A CONTROL UNIT	1,2,4		1 PER CKT
900M INPUT AND OUTPUT IMPEDANCES	5		1 PER CKT
600M INPUT AND OUTPUT IMPEDANCES	6		1 PER CKT

RECORD OF APP FIGURES, WIRING AND APPARATUS CHANGES									
IF JOB CHARGED ON ISS				USE IN CIRCUIT					
RECORDS	OPTION	SEE	NOTE	STD	ADM	NO			
DO NOT									
304-151									
108	Z OR Y	Z		Y		Z			
118	X OR U	X		U		X			
158	V OR U	V		U		V			

NETWORK VALUES		
NET-ONE NO.	RESISTANCE IN OHMS	CAPACITANCE IN UF
1	470	0.11

CIRCUIT NOTES: (CONT)

105. PRIOR TO ISSUE 40, THE 60A CONTROL UNIT WAS CODED APPARATUS. SOME 60A CONTROL UNITS WERE MANUFACTURED WITHOUT NETWORK(S) AND (Y). FOR INSTALLATION IN ESS NO. 1 OFFICE IT SHALL BE NECESSARY TO INSPECT 60A CONTROL UNITS PRIOR TO INSTALLATION FOR NETWORKS AND IF NOT PRESENT TO PROVIDE 185A NETWORKS AS SHOWN IN FST.

106. PRIOR TO ISSUE 140, QUIET TERMINATIONS (P₁, P₂, C1A, C1B) WERE REQUIRED ON THE T RELAY. THESE QUIET TERMINATIONS SHOULD BE REMOVED WHEN UPDATING THE CONTROL UNIT WITH CPT, ED-9W65-32 OR CPTA, ED-9W65-33.

107. PRIOR TO ISSUE 140, TERMINALS 17 AND 18 OF T81 WERE CONNECTED TO THE S RELAY. TERMINALS 17 AND 18 OF T81 SHOULD BE CONNECTED TO PINS L AND M OF J1 WHEN UPDATING THE CONTROL UNIT WITH CPT, ED-9W65-32 OR CPTA, ED-9W65-33.

EQUIPMENT NOTES:

201. THE 60A CONTROL UNIT REQUIRES 2 X 23 INCHES OF MOUNTING PLATE SPACE.

202. IN NO. 1 AND NO. 2 ESS OFFICES, THE 60A CONTROL UNIT SHOULD BE MOUNTED ON THE SAME MISCELLANEOUS TRUNK FRAME, AS THE ASSOCIATED COMBINED MILLIWATT AND LOOP AROUND TEST CIRCUIT.

203. IN NO. 1 ESS, THESE LEADS ARE TO BE CONNECTED TO THE REMOTE MASTER SCANNER APPLIQUE CIRCUIT. SD-1A210-01, OR MASTER SCANNER APPLIQUE CIRCUIT, SD-1A133-01. THE SCAM POINT GLASS IS TO BE "DIRECTED". REFER TO SD-1A272-01 FOR METHOD OF ASSIGNMENT. IN NO. 2 ESS, THESE LEADS SHOULD BE CONNECTED TO THE REMOTE MASTER SCANNER APPLIQUE CIRCUIT, SD-1A210-01, OR THE MASTER SCANNER APPLIQUE CIRCUIT, SD-1A133-01. SEE SD-20467-01 SECTION 1, 2, RULE 5B2 FOR THE SCANNER ASSIGNMENT IN THE VARIABLE ALARM.

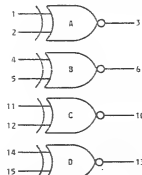
204.

ISSUE
168

J99329A CONTROL UNIT (60A) CIRCUIT		SD-99331-01-01
BELL TELEPHONE LABORATORIES HAWTHORNE, N.J.	65	PRINTED IN U.S.A.

INFORMATION NOTES:

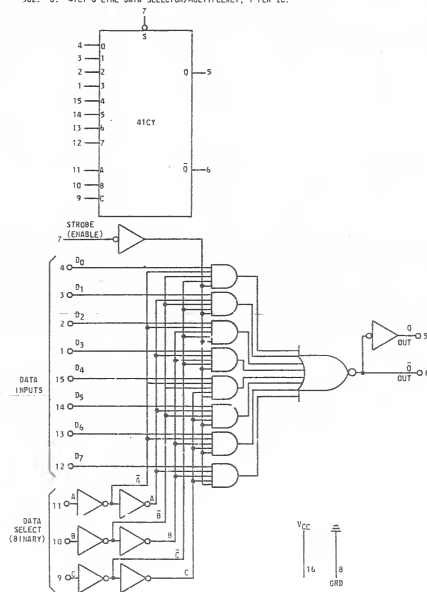
101. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
K IS FOR KILOHMS,
CAPACITANCE VALUES ARE IN MICROFARADS,
VALUES PRECEDED BY THE SYMBOL (+) OR - (MINUS) ARE IN VOLTS.
102. THE TERMINAL NUMBER ASSIGNMENT FOR THE
INTEGRATED CIRCUITS ARE AS FOLLOWS:
A. 41CT QUAD EXCLUSIVE NOR GATE, 4 PER IC.



INPUT/OUTPUT INFORMATION

INFORMATION NOTES: (CONT)

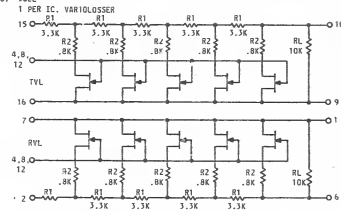
302. B. 41CY 8 LINE DATA SELECTOR/MULTIPLEXER, 1 PER IC.



INPUT/OUTPUT INFORMATION

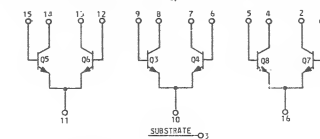
INFORMATION NOTES: (CONT)

302. C. 502L



INPUT/OUTPUT INFORMATION

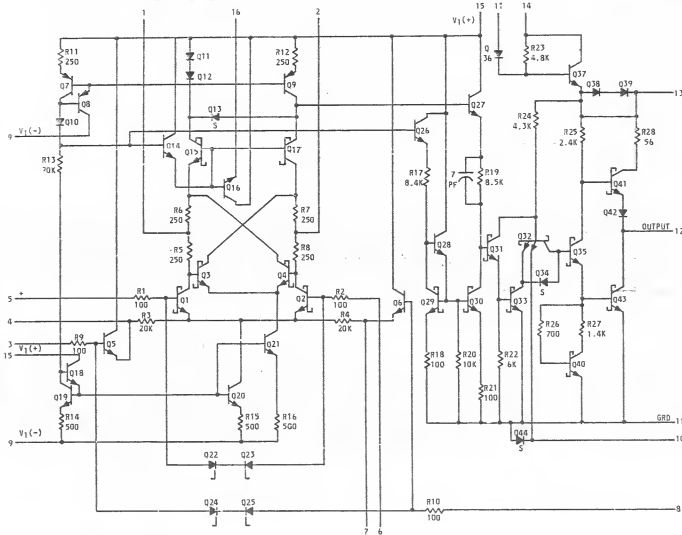
302. D. 502R DIFFERENTIAL TRANSISTOR PAIRS, 3 PER IC.



INPUT/OUTPUT INFORMATION

INFORMATION NOTES: (CONT)

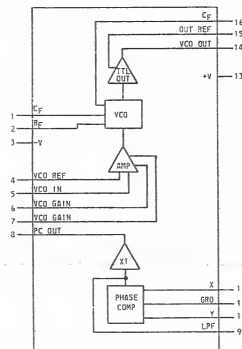
102. E. 502HG HIGH GAIN COMPARATOR.
1 PER IC.



INPUT/OUTPUT INFORMATION

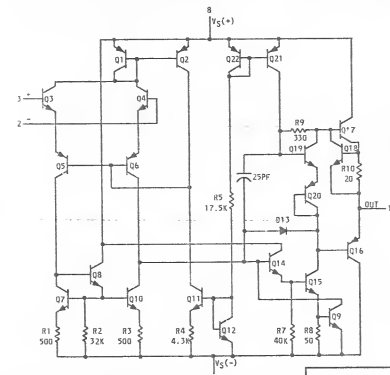
INFORMATION NOTES: (CONT)

102. F. 502EP PHASE-LOCKED LOOP.
1 PER IC.



INPUT/OUTPUT INFORMATION

102. G. 613F OPERATIONAL AMPLIFIER
2 PER IC.



INPUT/OUTPUT INFORMATION

J99329A CONTROL UNIT (406)
CIRCUIT

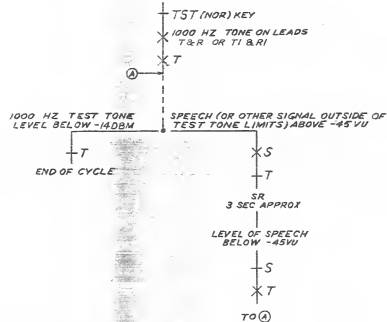
BELL TELEPHONE LABORATORIES
INCORPORATED

SD-99331-01-D3

65

ISSUE
16B

SC 1 (MFR DISC)
AUTOMATIC CONTROL



ISSUE		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40		
2A	2B	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40		
3A	3B	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40			
4A	4B	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40				
5A	5B	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40					
6A	6B	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40						
7A	7B	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40							
8A	8B	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40								
9A	9B	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40									
10A	10B	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40										
11A	11B	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40											
12A	12B	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40												
13A	13B	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40													
14A	14B	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40														
15A	15B	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40															
16A	16B	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																
17A	17B	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																	
18A	18B	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																		
19A	19B	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																			
20A	20B	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																				
21A	21B	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																					
22A	22B	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																						
23A	23B	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																							
24A	24B	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																								
25A	25B	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40																									
26A	26B	27	28	29	30	31	32	33	34	35	36	37	38	39	40																										
27A	27B	28	29	30	31	32	33	34	35	36	37	38	39	40																											
28A	28B	29	30	31	32	33	34	35	36	37	38	39	40																												
29A	29B	30	31	32	33	34	35	36	37	38	39	40																													
30A	30B	31	32	33	34	35	36	37	38	39	40																														
31A	31B	32	33	34	35	36	37	38	39	40																															
32A	32B	33	34	35	36	37	38	39	40																																
33A	33B	34	35	36	37	38	39	40																																	
34A	34B	35	36	37	38	39	40																																		
35A	35B	36	37	38	39	40																																			
36A	36B	37	38	39	40																																				
37A	37B	38	39	40																																					
38A	38B	39	40																																						
39A	39B	40																																							
40A	40B																																								

J993298 CONTROL UNIT (60A) CIRCUIT		SD-993 31-01-E1
BELL TELEPHONE LABORATORIES INCORPORATED		

SD-99331-01

CIRCUIT REQUIREMENTS

60A CONTROL UNIT CIRCUIT

APPARATUS			MECK REOT			CIRCUIT PREPARATION			TEST SET			DIRECT CURRENT FLOW REOT			REMARKS
DESIG	CODE	OPT	FIG.	REP	CONT	ARM	BLOCK	TEST	TEST	TEST	TEST	TEST	TEST	TEST	
				FIG.	PREP	TRVL	OR	CONN	BAT.	-CONN	GRD	WOG	FOF	MA	MA
RELAY															
S	1/2	2433	1	209				10(5)	20(5)	NSB	1,2	0		10,9	10,4
MTD WITH (1)															
T	1/2	2433	1	209				1L(1)	2L(1)	NSB	1,2	0		12,2	12,6
MTD WITH (5)															

TEST NOTES:

1. DISCONNECT CO BAT. FROM 60A CONTROL UNIT AND REMOVE PRINTED WIRING CARD BEFORE PERFORMING TEST.
2. POLARITY FOR TEST SET CONNECTION: BAT. (-); GRD (+).

J99329A CONTROL UNIT (60A)
CIRCUIT

SD-99331-01-FI

BELL TELEPHONE LABORATORIES
INCORPORATED

REV. 100-10-10-10

ISSUE
168J99329A CONTROL UNIT (60A)
CIRCUIT

SD-99331-01-FI

BELL TELEPHONE LABORATORIES
INCORPORATED

2

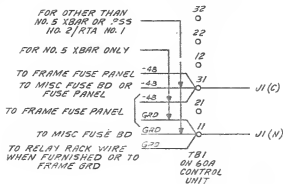
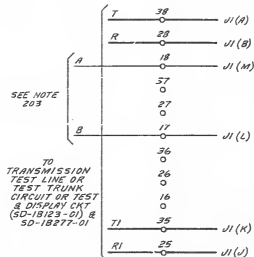
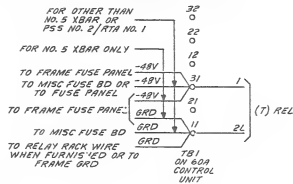
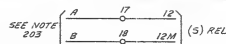
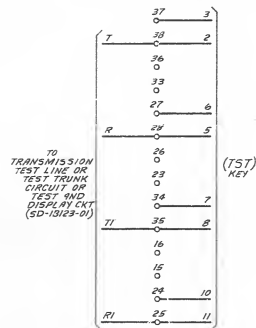
6S

REV. 100-10-10-10

ISSUE
168

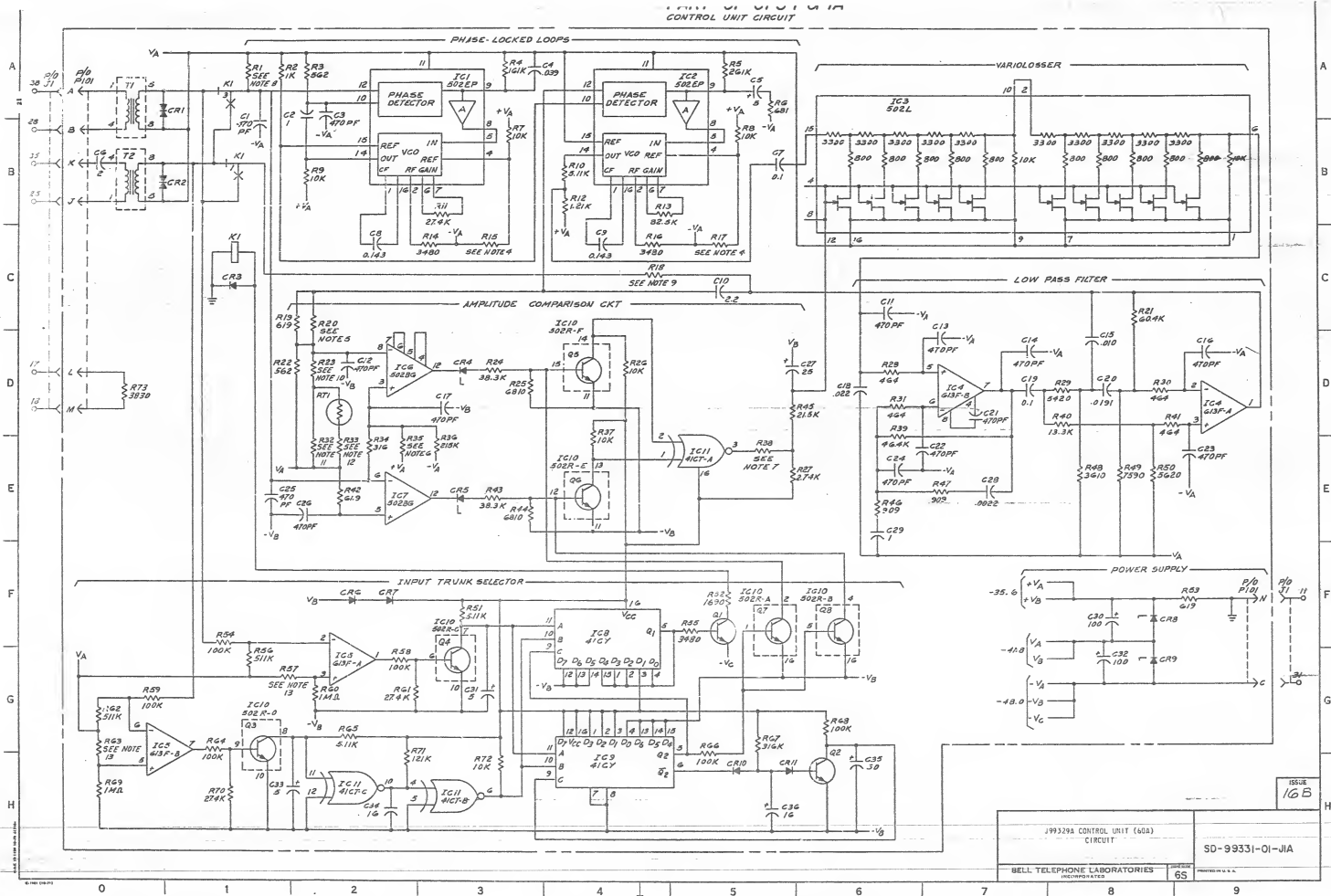
CAD 1 (MFR DISC) (FOR APP FIGS. 1, 3 AND 4)

CAD 2 (FOR APP FIGS. 5 AND 6)



ISSUE
16B

J19329B CONTROL UNIT (60A) CIRCUIT		SD-99331-01-GI
BELL TELEPHONE LABORATORIES INCORPORATED	6S	



COMPONENT LIST (CONT)
RESISTOR (CONT)

X SINGLE ELEMENT DEVICE

* SINGLE ELEMENT DEVICE

DESIGN	K1			
CODE	MA1A			
OPTION				
	CONT ARR	LOC	CONT ARR	LOC
4	EBM			
3	EBM	J1A/A1		
2	EBM			
1	EBM	J1A/B1		
COIL		J1A/C1		

DESIG	CODE
C29	600A, 1
C30	602B, 100
C31	600B, 5
C32	602B, 100
C33	600B, 5
C34	603B, 16
C35	604B, 50
C36	603B, 16

<u>OESIG</u>	<u>CODE</u>
R1	KS-20616,
R2	KS-20616,
R3	KS-20616,
R4	KS-20616,
R5	KS-20616,
R6	KS-20616,
R7	KS-20616,
R8	KS-20616,

CEIG	CODE
C1	K5-19774, L2, 470PF
C2	K5-20736, L1, 2
C3	K5-19774, L2, 470PF
C4	K5-20300, L1, .039
C5	4601A, 5
C6	5424B, 2
C7	K5-20300, L3, 0.1
C8	5358N, 0.143
C9	5358N, 0.143
C10	K5-19774, L2, 2
C11	K5-19774, L2, 470PF
C12	K5-19774, L2, 470PF
C13	K5-19774, L2, 470PF
C14	K5-19774, L2, 470PF
C15	53515, .010
C16	K5-19774, L2, 470PF
C17	K5-19774, L2, 470PF
C18	K5-20300, L2, .032
C19	K5-20300, L3, 0.1
C20	73560, .0191
C21	K5-19774, L2, 470PF
C22	K5-19774, L2, 470PF
C23	K5-19774, L2, 470PF
C24	K5-19774, L2, 470PF
C25	K5-19774, L2, 470PF
C26	K5-19774, L2, 470PF
C27	4601B, .25
C28	K5-20300, L1, .0027

<u>DESIG</u>	<u>CODE</u>
P101	KS-16345,1

<u>DESIG</u>	<u>CODE</u>
CR1	521A
CR2	521A
CR3	533B
CR4	808B
CR5	808B
CR6	533L
CR7	533B
CR8	808B
CR9	808B
CR10	458C
CR11	458C

R13	KS-20816
R14	2578, 348
R15	KS-20816
R16	2578, 348
R17	KS-20816
R18	KS-20816
R19	KS-16311
R20	KS-20815
R21	KS-20816
R22	KS-16311
R23	KS-20816
R24	KS-20816
R25	KS-20816
R26	KS-20816
R27	KS-20816
R28	KS-20816
R29	KJ-20816
R30	KS-20816
R31	KS-26816
R32	KS-20816
R33	KS-20816
R34	KS-20816
R35	KS-20816
R36	KS-20816
R37	KS-20816
R38	KS-20816
R39	KS-20816
R40	KS-20816
R41	KS-20816
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R45	KS-20816
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R47	KS-20816
R48	KS-20816
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R93	KS-20816
R94	KS-20816
R95	KS-20816
R96	KS-20816
R97	KS-20816
R98	KS-20816
R99	KS-20816
R100	KS-20816

THERMISTOR

RT1 17A

<u>OE510</u>	<u>CODE</u>
T1	25648
T2	25688

DESIG	CODE
Q1	660
Q2	660



- NOTES:

1. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
K IS FOR KILOHMS,
CAPACITANCE VALUES ARE IN MICROFARADS,
VALUES PRECEDED BY THE SYM BOL + (PLUS)
OR - (MINUS) ARE IN VOLTS.
2.  GROUND RETURN.
3. BATTERY AND GROUND TERMINATIONS FOR IC'S:

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